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1 Tempest and typhoon: user-level shared memory

S. K. Reinhardt, J. R. Larus, D. A. Wood

April 1994 ACM SIGARCH Computer Architecture News, Proceedings of the 21ST annual international symposium on Computer architecture, Volume 22 Issue 2

Full text available: odf(1.44 MB)

Additional Information: full citation, abstract, references, citings, index terms

Future parallel computers must efficiently execute not only hand-coded applications but also programs written in high-level, parallel programming languages. Today's machines limit these programs to a single communication paradigm, either message-passing or sharedmemory, which results in uneven performance. This paper addresses this problem by defining an interface, Tempest, that exposes low-level communication and memory-system mechanisms so programmers and compilers can customize polici ...

2 Tempest and typhoon: user-level shared memory.

Steven K. Reinhardt, James R. Larus, David A. Wood

August 1998 25 years of the international symposia on Computer architecture (selected papers)

Full text available: gdi(1.57 MB)

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3 Firefly: a multiprocessor workstation

Charles P. Thacker, Lawrence C. Stewart

October 1987 Proceedings of the second international conference on Architectual support for programming languages and operating systems, Volume 15, 22, 21 Issue 5, 10, 4

Full text available: pdf(1.10 MB)

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Firefly is a shared-memory multiprocessor workstation that contains from one to seven MicroVAX 78032 processors, each with a floating point unit and a sixteen kilobyte cache. The caches are coherent, so that all processors see a consistent view of main memory. A system may contain from four to sixteen megabytes of storage. Input-output is done via a standard DEC QBus. Input-output devices are an Ethernet controller, fixed disks, and a monochrome 1024 x 768 display with keyboard and mouse. Option ...

Delayed consistency and its effects on the miss rate of parallel programs Michel Dubois, Jin Chin Wang, Luiz A. Barroso, Kangwoo Lee, Yung-Syau Chen August 1991 Proceedings of the 1991 ACM/IEEE conference on Supercomputing



Full text available: doi: (1.01 MB)

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5 The V distributed system

David Cheriton

March 1988 Communications of the ACM, Volume 31 Issue 3

Full text available: pdf(2.55 MB)



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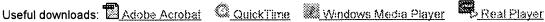
The V distributed System was developed at Stanford University as part of a research project to explore issues in distributed systems. Aspects of the design suggest important directions for the design of future operating systems and communication systems.

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Print Format	[Abstract] [PDF Full-Text (963 KB)] IEEE CNF
	3 Speculative data-driven multithreading Roth, A.; Sohi, G.S.; High-Performance Computer Architecture, 2001. HPCA. The Seventh Internat Symposium on , 19-24 Jan. 2001 Pages:37 - 48
	[Abstract] [PDF Full-Text (1148 KB)] IEEE CNF
	4 Scalable Web server architectures

Mourad, A.; Huiqun Liu;

Computers and Communications, 1997. Proceedings., Second IEEE Symposiu on , 1-3 July 1997

Pages:12 - 16

[Abstract] [PDF Full-Text (484 KB)] IEEE CNF